

ABSTRACT OF THE DISCLOSURE

An electro-optical panel driving circuit is provided on a substrate and includes a shift register circuit that sequentially outputs transfer signals, a buffer circuit that buffers the sequentially output transfer signals, a sampling circuit that samples image signals using the buffered transfer signals as sampling pulses and that supplies the sampled image signals to data lines, and a dummy circuit that simulates at least part of the buffer circuit and the sampling circuit. Delay signals indicating the amount of delay of the sampling pulses and generated by the dummy circuit are fed back to the shift register circuit so that the amount of delay is reduced. The buffer circuit, the sampling circuit, and the dummy circuit are provided on the substrate.